

Z10PR-D16 Repair Guide

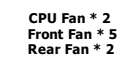
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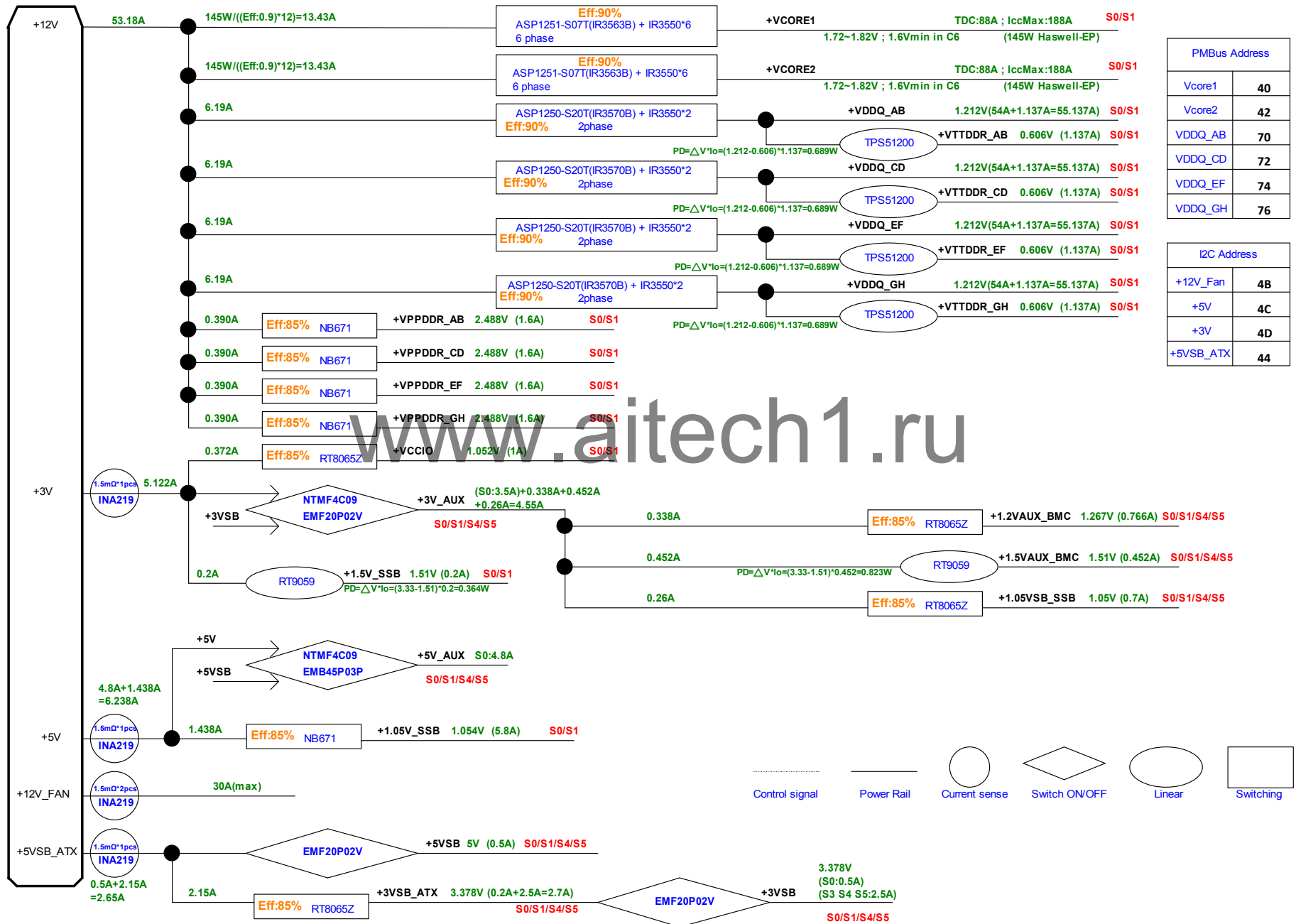
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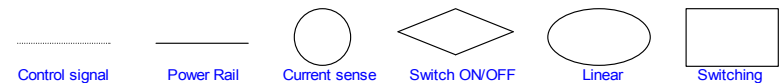


POWER FLOW



PMBus Address	
Vcore1	40
Vcore2	42
VDDQ_AB	70
VDDQ_CD	72
VDDQ_EF	74
VDDQ_GH	76

I2C Address	
+12V_Fan	4B
+5V	4C
+3V	4D
+5VSB_ATX	44



Power Sequence

1. VBAT		21. PCH_SUSACK#	46. CPLD_PCH_PWROK
1 -> 2 : > 9 ms		22. R_FP_PWRBNT#	47. PCH_DRAMPWROK
2. PCH_RTCRST#		22 -> 23 : = 64 ms	48. CPLD_CLKEN
3. CPU1_SKTOCC#/CPU2_SKTOCC#		23. SIO_PSOUT#	49. PCH internal CLK OUT
4. Power Plug		24. R_PCH_GPIO63 (SLP_S5#)	50. DB1900Z CLK OUT
5. +5VSB_ATX		24 -> 25 : > 30 us	51. CPLD_SYS_PWROK
5 -> 6 : = 0 ms		25. PCH_SLPS4#	52. PCH_PROCPWRGD
6. +3VSB_ATX		25 -> 26 : > 30 us	53. CPLD_CPU1_PWRGOOD
6 -> 7 : > 10 ms		26. PCH_SLPS3#	CPLD_CPU2_PWRGOOD
7. +3VSB_ATX_PWROK		27. SIO_PSON#	54. PCH_PLTRST#
7 -> 8 : > 95 ms		28. ATXPSON#	55. CPU1_SVID_CLK/DATA
8. PCH_SLPSUS#		BMC_ATXPSON#	CPU2_SVID_CLK/DATA
8 -> 9 : < 4.7 ms		29. +3V/+5V/+12V/-12V	
9. +5VSB		29 -> 30 : 300 ~ 500 ms	
9 -> 10 : = 0 ms		30. ATXPGD	
10. +3VSB		31. SIO_PWROK	
11. +3V_AUX (VSB)		32. +3V_AUX (MAIN)	
11 -> 12 : = 500 us	+3V ←	33. +1.05V_SSB	
12. +1.5VAUX_BMC		34. +1.05V_SSB_PWRGD	
+1.8VAUX_LAN (I350)		35. CPLD_VCCIO_EN	
12 -> 13 : = 500 us	+5V ←	36. +VCCIO	
13. +1.2VAUX_BMC		37. +VCCIO_PWRGD	
+1.0VAUX_LAN (I350)	+1.05V_SSB ←	38. +1.5V_SSB	
13 -> 14 : = 2.5 ms		39. +1.5V_SSB_PWRGD	
14. +1.2VAUX_BMC_PWROK	+1.5V_SSB, VCCIO ←	40. +VPPDDR_AB/CD/EF/GH	
15. BMC_DDR3_RESET#		41. CPLD_DDR_CPU1_EN	
BMC_RESET#		CPLD_DDR_CPU2_EN	
6 -> 16 : = 250 ms	+VPPDDR_AB/CD/EF/GH ←	+VDDQ_AB/CD/EF/GH	
16. SIO_RSMRST#		+VDDQ_AB/CD/EF/GH_PWRGD	
16 -> 17 : > 100 ms		42. +VTTDDR_AB/CD/EF/GH	
17. PCH_SLPA#		43. CPLD_VCORE1_EN	
18. +1.05VSB_SSB		CPLD_VCORE2_EN	
+1.05VSB_SSB_PWRGD		44. +VCORE1/+VCORE2	
19. CPLD_APWROK		45. +VCORE1_PWRGD	
CPLD_PSON		+VCORE2_PWRGD	
20. PCH_SUSWARN#			

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Clock Distribution

